## EC1145HSET-57.273M-G



ELECTRICAL SPECIFICATIONS

| Nominal Frequency | 57.273 MHz |
| :--- | :--- |
| Frequency Tolerance/Stability | $\pm 50 \mathrm{ppm}$ Maximum (Inclusive of all conditions: Calibration Tolerance at $25^{\circ} \mathrm{C}$, Frequency Stability over the <br> Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at $25^{\circ} \mathrm{C}$, <br> Shock, and Vibration) |
| Aging at 25 ${ }^{\circ} \mathrm{C}$ | $\pm 5 \mathrm{ppm} /$ year Maximum |
| Operating Temperature Range | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Supply Voltage | $5.0 \mathrm{Vdc} \pm 10 \%$ |
| Input Current | 65 mA Maximum |
| Output Voltage Logic High (Voh) | 2.4 Vdc Minimum with TTL Load, Vdd-0.5Vdc Minimum with HCMOS Load |
| Output Voltage Logic Low (Vol) | 0.4 Vdc Maximum with TTL Load, 0.5 Vdc Maximum with HCMOS Load |
| Rise/Fall Time | 6 nSec Maximum (Measured at 0.4 Vdc to 2.4 Vdc with TTL Load; Measured at 20\% to $80 \%$ of waveform <br> with HCMOS Load) |
| Duty Cycle | $50 \pm 10(\%)$ (Measured at 1.4Vdc with TTL Load or at $50 \%$ of Waveform with HCMOS Load) |
| Load Drive Capability | 10 TTL Load or 15 pF HCMOS Load Maximum |
| Output Logic Type | CMOS |
| Pin $\mathbf{1}$ Connection | No Connect |
| Tri-State Input Voltage (Vih and Vil) | +2.2 Vdc Minimum to enable output, +0.8 Vdc to disable output (High Impedance), No connect to enable <br> output. |
| Absolute Clock Jitter | $\pm 100 \mathrm{pSec}$ Maximum |
| One Sigma Clock Period Jitter | $\pm 25 \mathrm{pSec} \mathrm{Maximum}$ |
| Start Up Time | 10 mSec Maximum |
| Storage Temperature Range | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |

ENVIRONMENTAL \& MECHANICAL SPECIFICATIONS

| Fine Leak Test | MIL-STD-883, Method 1014, Condition A |
| :--- | :--- |
| Gross Leak Test | MIL-STD-883, Method 1014, Condition C |
| Lead Integrity | MIL-STD-883, Method 2004 |
| Mechanical Shock | MIL-STD-202, Method 213, Condition C |
| Resistance to Soldering Heat | MIL-STD-202, Method 210 |
| Resistance to Solvents | MIL-STD-202, Method 215 |
| Solderability | MIL-STD-883, Method 2003 |
| Temperature Cycling | MIL-STD-883, Method 1010 |
| Vibration | MIL-STD-883, Method 2007, Condition A |

## EC1145HSET-57.273M-G

MECHANICAL DIMENSIONS (all dimensions in millimeters)


| PIN | CONNECTION |
| :--- | :--- |
| 1 | No Connect |
| 4 | Case Ground |
| 5 | Output |
| 8 | Supply Voltage |


| LINE |  |
| :--- | :--- |
| 1 | MARKING |
| 1 | ECLIPTEK |
| 2 | EC11 <br> EC11=Product Series |
| 3 | $57.273 M$ |$|$| XXYZZ |
| :--- |
| XX=Ecliptek Manufacturing |
| Code |
| Y=Last Digit of Year |
| ZZ=Week of Year |

## OUTPUT WAVEFORM



## Test Circuit for TTL Output

| Output Load <br> Drive Capability | $\mathbf{R}_{\mathbf{L}}$ Value <br> (Ohms) | $\mathbf{C}_{\mathbf{L}}$ Value <br> $(\mathbf{p F})$ |
| :---: | :---: | :---: |
| $10 T T \mathrm{~L}$ | 390 | 15 |
| $5 T T \mathrm{t}$ | 780 | 15 |
| 2 TTL | 1100 | 6 |
| 10 LSTTL | 2000 | 15 |
| 1 TTL | 2200 | 3 |

Table 1: $R_{L}$ Resistance Value and $C_{L}$ Capacitance Value Vs. Output Load Drive Capability


Note 1: An external $0.1 \mu \mathrm{~F}$ low frequency tantalum bypass capacitor in parallel with a $0.01 \mu \mathrm{~F}$ high frequency ceramic bypass capacitor close to the package ground and $\mathrm{V}_{\mathrm{DD}}$ pin is required.
Note 2: A low capacitance ( $<12 \mathrm{pF}$ ), 10X attenuation factor, high impedance ( $>10 \mathrm{Mohms}$ ), and high bandwidth ( $>300 \mathrm{MHz}$ ) passive probe is recommended.
Note 3: Capacitance value $C_{L}$ includes sum of all probe and fixture capacitance.
Note 4: Resistance value $\mathrm{R}_{\mathrm{L}}$ is shown in Table 1. See applicable specification sheet for 'Load Drive Capability'.
Note 5: All diodes are MMBD7000, MMBD914, or equivalent.

## Test Circuit for CMOS Output



Note 1: An external $0.1 \mu \mathrm{~F}$ low frequency tantalum bypass capacitor in parallel with a $0.01 \mu \mathrm{~F}$ high frequency ceramic bypass capacitor close to the package ground and $\mathrm{V}_{\mathrm{DD}}$ pin is required.
Note 2: A low capacitance ( $<12 \mathrm{pF}$ ), 10X attenuation factor, high impedance ( $>10 \mathrm{Mohms}$ ), and high bandwidth ( $>300 \mathrm{MHz}$ ) passive probe is recommended.
Note 3: Capacitance value $\mathrm{C}_{\mathrm{L}}$ includes sum of all probe and fixture capacitance.

## EC1145HSET-57.273M-G

Recommended Solder Reflow Methods


High Temperature Solder Bath (Wave Solder)

| Ts MAX to $\mathrm{T}_{\mathrm{L}}$ (Ramp-up Rate) | $3^{\circ} \mathrm{C} /$ second Maximum |
| :---: | :---: |
| Preheat |  |
| - Temperature Minimum ( $\mathrm{T}_{S} \mathrm{MIN}$ ) | $150^{\circ} \mathrm{C}$ |
| - Temperature Typical (TSTYP) | $175{ }^{\circ} \mathrm{C}$ |
| - Temperature Maximum (Ts MAX) | $200^{\circ} \mathrm{C}$ |
| - Time ( $\mathrm{ts}_{\text {S MIN} \text { ) }}$ | 60-180 Seconds |
| Ramp-up Rate ( $\mathrm{T}_{\mathrm{L}}$ to $\mathrm{T}_{\mathrm{P}}$ ) | $3^{\circ} \mathrm{C} /$ second Maximum |
| Time Maintained Above: |  |
| - Temperature ( $\mathrm{T}_{\mathrm{L}}$ ) | $217^{\circ} \mathrm{C}$ |
| - Time ( $\mathrm{t}_{\mathrm{L}}$ ) | 60-150 Seconds |
| Peak Temperature ( $\mathrm{T}_{\mathrm{P}}$ ) | $260^{\circ} \mathrm{C}$ Maximum for 10 Seconds Maximum |
| Target Peak Temperature ( $\mathrm{T}_{\mathrm{P}}$ Target) | $250^{\circ} \mathrm{C}+0 /-5^{\circ} \mathrm{C}$ |
| Time within $5^{\circ} \mathrm{C}$ of actual peak ( $\mathrm{t}_{\mathrm{p}}$ ) | 20-40 seconds |
| Ramp-down Rate | $6^{\circ} \mathrm{C} /$ second Maximum |
| Time $25^{\circ} \mathrm{C}$ to Peak Temperature (t) | 8 minutes Maximum |
| Moisture Sensitivity Level | Level 1 |

## EC1145HSET-57.273M-G

## Recommended Solder Reflow Methods



Low Temperature Infrared/Convection $185^{\circ} \mathrm{C}$

| $\mathrm{T}_{\mathrm{S}}$ MAX to $\mathrm{T}_{\mathrm{L}}$ (Ramp-up Rate) | $5^{\circ} \mathrm{C} /$ second Maximum |
| :---: | :---: |
| Preheat |  |
| - Temperature Minimum ( $\mathrm{T}_{\mathrm{s}} \mathrm{MIN}$ ) | N/A |
| - Temperature Typical (Ts TYP) | $150^{\circ} \mathrm{C}$ |
| - Temperature Maximum (TS MAX) | N/A |
| - Time ( $\mathrm{ts}_{\text {S }}$ MIN) | 60-120 Seconds |
| Ramp-up Rate ( $\mathrm{T}_{\mathrm{L}}$ to $\mathrm{T}_{\mathrm{P}}$ ) | $5^{\circ} \mathrm{C} /$ second Maximum |
| Time Maintained Above: |  |
| - Temperature ( $\mathrm{T}_{\mathrm{L}}$ ) | $150^{\circ} \mathrm{C}$ |
| - Time ( $\mathrm{t}_{\mathrm{L}}$ ) | 200 Seconds Maximum |
| Peak Temperature ( $\mathrm{T}_{\mathrm{P}}$ ) | $185^{\circ} \mathrm{C}$ Maximum |
| Target Peak Temperature ( $\mathrm{T}_{\mathrm{P}}$ Target) | $185^{\circ} \mathrm{C}$ Maximum 2 Times |
| Time within $5^{\circ} \mathrm{C}$ of actual peak ( $\mathrm{t}_{\mathrm{p}}$ ) | 10 seconds Maximum 2 Times |
| Ramp-down Rate | $5^{\circ} \mathrm{C} /$ second Maximum |
| Time $25^{\circ} \mathrm{C}$ to Peak Temperature (t) | N/A |
| Moisture Sensitivity Level | Level 1 |

## EC1145HSET-57.273M-G

## Recommended Solder Reflow Methods



Low Temperature Solder Bath (Wave Solder)

| $\mathrm{T}_{\mathrm{S}}$ MAX to $\mathrm{T}_{\mathrm{L}}$ (Ramp-up Rate) | $5^{\circ} \mathrm{C} /$ second Maximum |
| :---: | :---: |
| Preheat |  |
| - Temperature Minimum ( $\mathrm{T}_{\mathbf{s}} \mathrm{MIN}$ ) | N/A |
| - Temperature Typical (Ts TYP) | $150^{\circ} \mathrm{C}$ |
| - Temperature Maximum (Ts MAX) | N/A |
| - Time ( $\mathrm{t}_{\mathrm{s}}$ MIN) | 30-60 Seconds |
| Ramp-up Rate ( $\mathrm{L}_{\mathrm{L}}$ to $\mathrm{T}_{\mathrm{P}}$ ) | $5^{\circ} \mathrm{C} /$ second Maximum |
| Time Maintained Above: |  |
| - Temperature ( $\mathrm{T}_{\mathrm{L}}$ ) | $150^{\circ} \mathrm{C}$ |
| - Time ( $\mathrm{t}_{\mathrm{L}}$ ) | 200 Seconds Maximum |
| Peak Temperature ( $\mathrm{T}_{\mathrm{P}}$ ) | $245^{\circ} \mathrm{C}$ Maximum |
| Target Peak Temperature ( $\mathrm{T}_{\mathrm{P}}$ Target) | $245^{\circ} \mathrm{C}$ Maximum 1 Time / $235^{\circ} \mathrm{C}$ Maximum 2 Times |
| Time within $5^{\circ} \mathrm{C}$ of actual peak ( $\mathrm{t}_{\mathrm{p}}$ ) | 5 seconds Maximum 1 Time / 15 seconds Maximum 2 Times |
| Ramp-down Rate | $5^{\circ} \mathrm{C} /$ second Maximum |
| Time $25^{\circ} \mathrm{C}$ to Peak Temperature (t) | N/A |
| Moisture Sensitivity Level | Level 1 |

## Low Temperature Manual Soldering

$185^{\circ} \mathrm{C}$ Maximum for 10 seconds Maximum, 2 times Maximum.
High Temperature Manual Soldering
$260^{\circ} \mathrm{C}$ Maximum for 5 seconds Maximum, 2 times Maximum.

